

FIG. 1 (Prior Art)

100

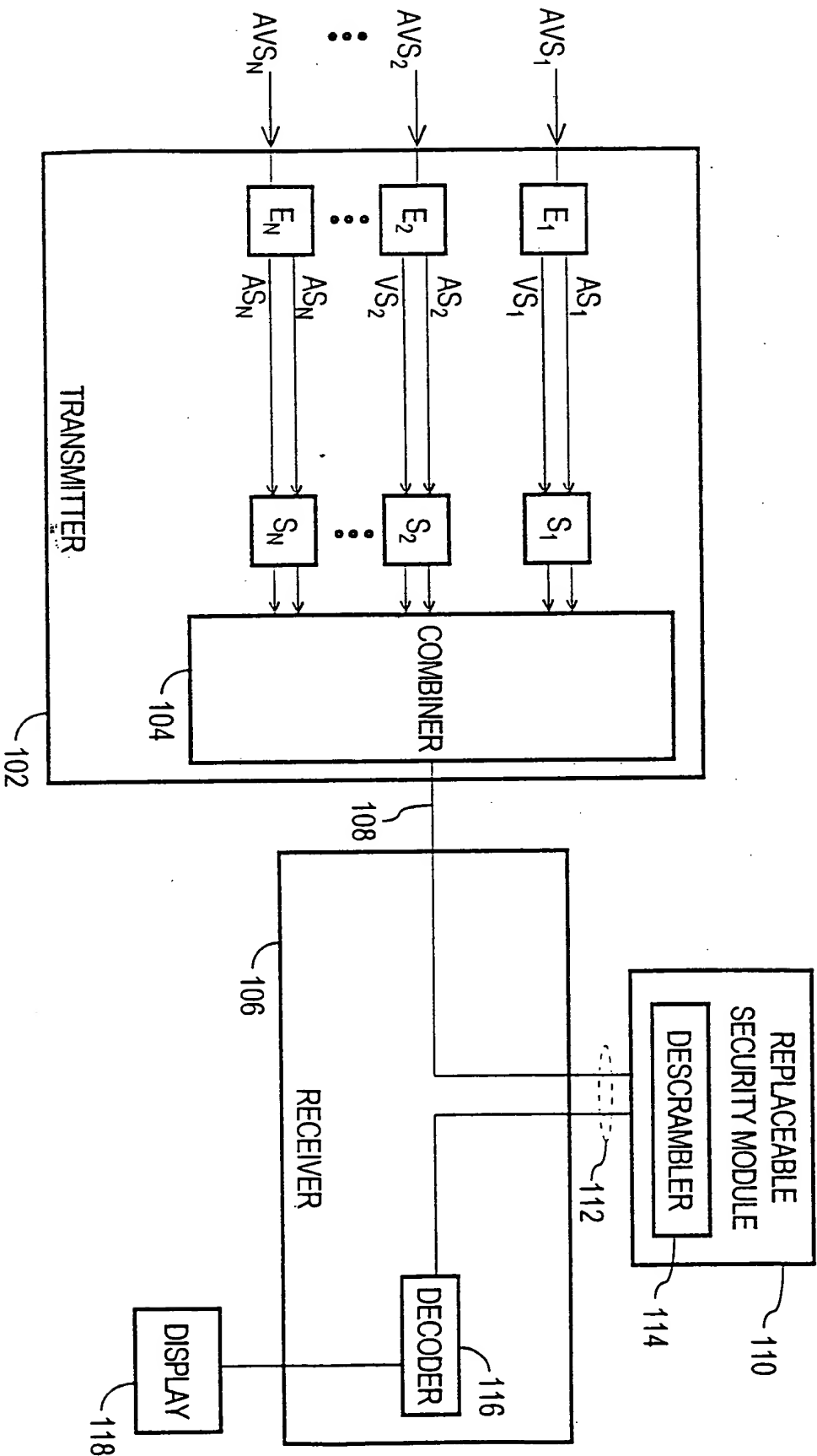
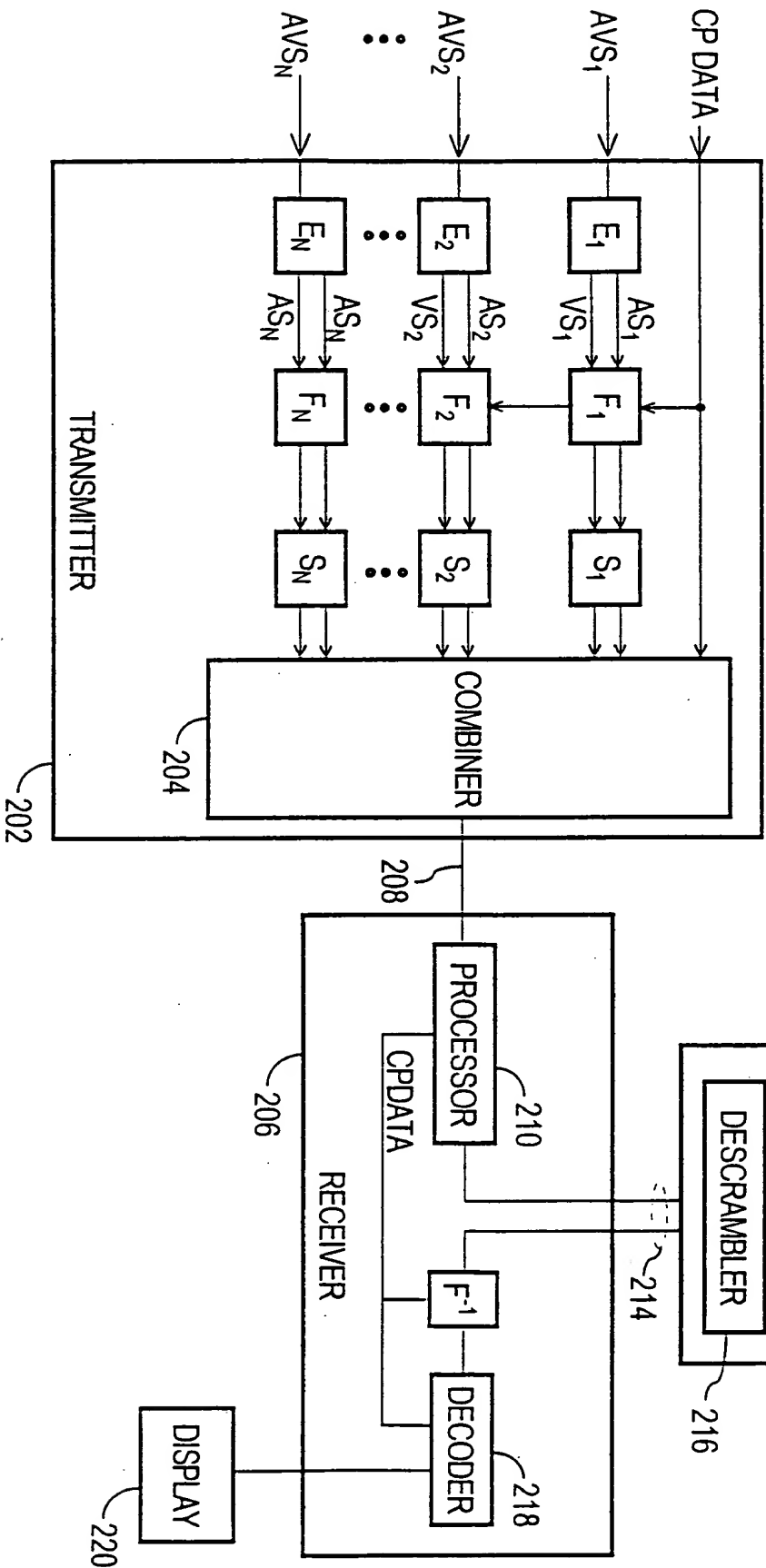


FIG. 2

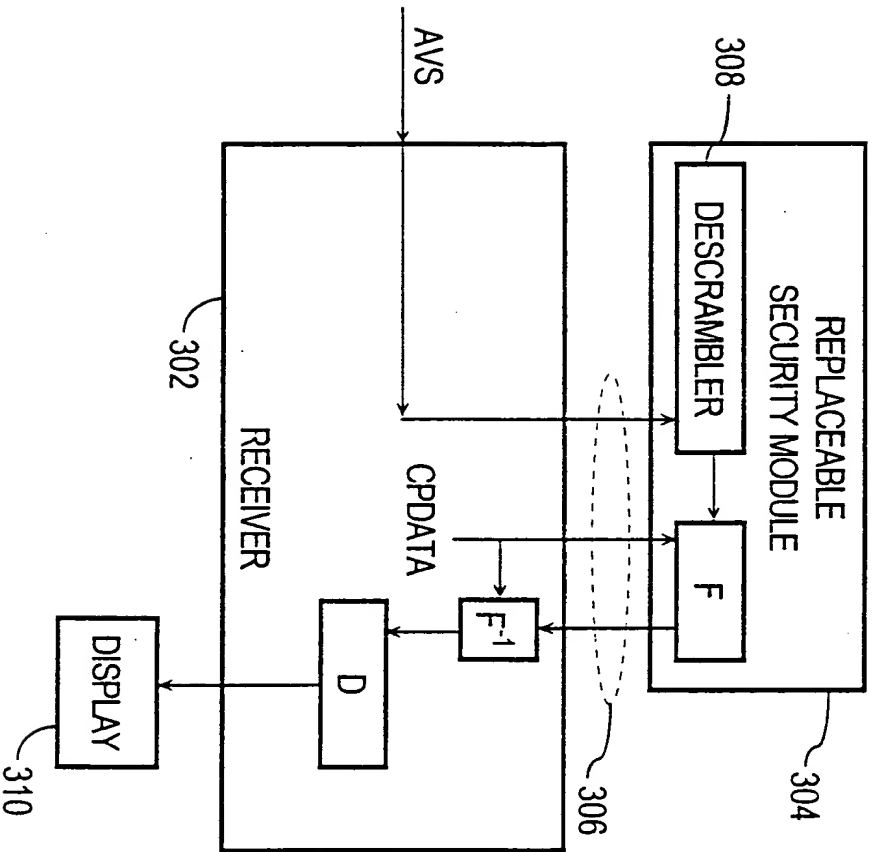
200



APPROVED	O.G. FIG.	CLASS	SUBCLASS
BY			
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FIG. 3

300
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FIG.4

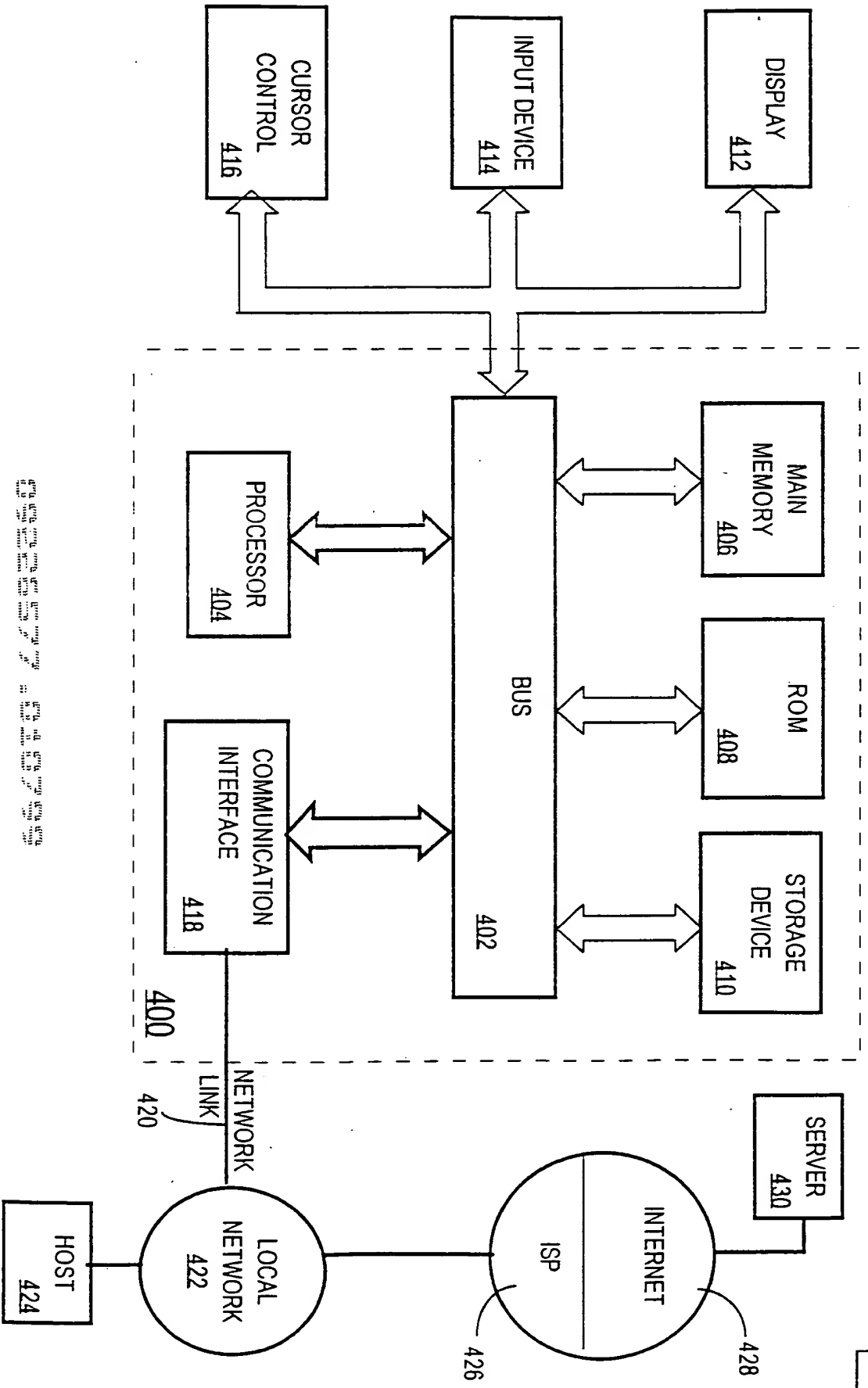


FIG. 4 is a block diagram of a computer system 400. The system 400 includes a display 412, an input device 414, a cursor control 416, a main memory 406, a ROM 408, a storage device 410, a processor 404, and a communication interface 418. The display 412, input device 414, cursor control 416, main memory 406, ROM 408, storage device 410, processor 404, and communication interface 418 are all connected to a bus 402. The communication interface 418 is connected to a network link 420, which is connected to a local network 422. The local network 422 is connected to an Internet 428 via an ISP 426. A server 430 is also connected to the Internet 428. A host 424 is connected to the local network 422.